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14 ALPHA & OMEGA SEMICONDUCTOR,
15 INC.

15 UNITED STATES DISTRICT COURT
16 NORTHERN DISTRICT OF CALIFORNIA
17 SAN FRANCISCO DIVISION
18

19 ALPHA & OMEGA SEMICONDUCTOR,
20 LTD., a Bermuda corporation; and
21 ALPHA & OMEGA SEMICONDUCTOR,
22 INC., a California corporation,

21 Plaintiffs and Counterdefendants,

22 v.

23 FAIRCHILD SEMICONDUCTOR
24 CORP., a Delaware corporation,

25 Defendant and Counterclaimant.

26 AND RELATED COUNTERCLAIMS.
27

Case No. C 07-2638 JSW
(Consolidated with Case No. C-07-2664 JSW)

**DECLARATION OF ANDREW J. WU IN
SUPPORT OF ALPHA & OMEGA
SEMICONDUCTOR, LTD AND ALPHA &
OMEGA SEMICONDUCTOR, INC'S
OPENING CLAIM CONSTRUCTION
BRIEF PURSUANT TO CIVIL L. R.
16-11(D)(1)**

Date: June 4, 2008
Time: 2:00 PM
Place: Courtroom 2, 17th Floor
Judge: Honorable Jeffrey S. White

1 I, Andrew J. Wu, hereby declare as follows:

2 1. I am a partner of the law firm of Morgan, Lewis & Bockius LLP, 2 Palo Alto
3 Square Avenue, 3000 El Camino Real, Suite 700, Palo Alto, California 94306, and a member in
4 good standing of the Bar of the State of California and Illinois. Morgan, Lewis & Bockius LLP
5 has been retained as trial counsel for the Plaintiffs and Counterdefendants Alpha & Omega
6 Semiconductor, LTD and Alpha & Omega Semiconductor, Inc., in the present action. I submit
7 this declaration in support of Alpha & Omega Semiconductor, LTD and Alpha & Omega
8 Semiconductor, Inc.'s Opening Claim Construction Brief Pursuant to Civil L. R. 16-11(d)(1).

9 2. Attached hereto as Exhibit 1 is a true and correct copy of United States Patent No.
10 5,767,567.

11 3. Attached hereto as Exhibit 2 is a true and correct copy of United States Patent No.
12 5,907,776.

13 4. Attached hereto as Exhibit 3 is a true and correct copy of United States Patent No.
14 5,930,630.

15 5. Attached hereto as Exhibit 4 is a true and correct copy of the definition of
16 "several," in the OXFORD ENGLISH DICTIONARY ONLINE, (downloaded on 03/12/2008).

17 6. Attached hereto as Exhibit 5 is a true and correct copy of page 1073 of MERRIAM
18 WEBSTER'S COLLEGIATE DICTIONARY (10th ed. 1997) (defining "several").

19 7. Attached hereto as Exhibit 6 is a true and correct copy of page 1324 of the
20 MICROSOFT ENCARTA COLLEGE DICTIONARY (2001) (defining "several").

21 8. Attached hereto as Exhibit 7 is a true and correct copy of pages 186 and 313 of
22 THE IEEE STANDARD DICTIONARY OF ELECTRICAL AND ELECTRONICS TERMS (6th ed. 1997)
23 (defining "compensated semiconductor" and "doping compensation").

24 9. Attached hereto as Exhibit 8 is a true and correct copy of page 102 of Donald A.
25 Neaman, *An Introduction to Semiconductor Devices* (2006) (defining "compensated
26 semiconductor").
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